A Report on

Webinar on Optimized Digital Logic Design and Testing



Date: 20 June, 2020

Time: 3:00 pm

Department of Physics

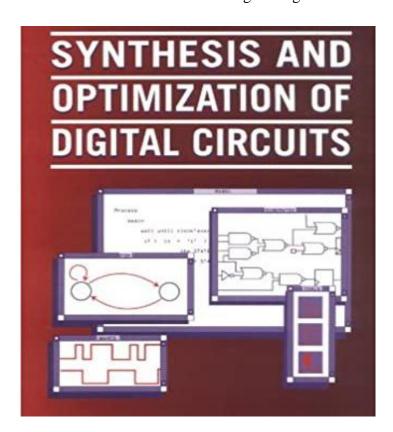
School of mathematical and Physical Sciences

Dr Harisingh Gour Vishwavidyalaya Sagar, MP



Dr. Fazal Noorbasha, Associate Professor, K.L. University Guntur A.P., delivered a lecture on Optimized Digital Logic Design and Testing on the 20th of June, 2020, in a webinar organized by department of Physics School of mathematical and Physical Sciences Dr Harisingh Gour Vishwavidyalaya Sagar, MP. The convener of the event was Prof. Ashish Verma, Dean School of mathematical and Physical Sciences the event was presided by honorable VC Prof. R. P. Tiwari sir. The inaugural and welcome speech was given by Prof. Ashish Verma. Prof. Diwakar Shukla delivered words of wisdom and then there was a speech from the side of Honorable VC sir.

During the technical session Dr. Fazal Noorbasha talked logic designs and their testing.



After the technical session there was questionnaire session and vote of thanks was delivered. The convener of the event was Prof. Ashish Verma. The anchoring of the session was done by Dr. Alka Dubey. The introduction of the speakers was read by Ms. Sherry Nasir..

Total number of participants registered were 109 out of which 102 participated in the webinar and 102 certificates were issued by the department.